**DINEESH PERIASAMY**

Unit G-4-2 Apt Taman Desa

Jln Desa Batu Caves 1/7

68100 Batu Caves, Selangor

Phone: 012-3491247

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**OBJECTIVE**

Seeking for an entry level position of electrical engineer where I can work with maximum potential in a challenging and dynamic environment, with an opportunity of working with diverse group of people and enhancing my professional skills with learning and experience for career growth.

**EDUCATION**

KBU International College, Bandar Utama, PJ

**Diploma In Electronic Engineering, 9/2012**

Major: Electronic fundamentals

CGPA 2.5

Sheffield Hallam University, UK

**Bachelor of Engineering (Honours) in Electrical and Electronic Engineering, 10/2015**

Major: Electrical and Electronic fundamentals

CGPA 3.0

**RELEVANT COURSES**

Digital Electronics, Circuit Theory, Analog Electronics/Circuits, Logic Design, Signals & Systems, Verilog Hardware Description Language (VHDL), Control Systems, Mobile Communications, VLSI, Embedded System Design & Digital Signal Processing

**KEY QUALIFICATIONS**

* **Excellent experience in circuit analysis**
* Proven ability to understand and analyse project requirements and translating into specifications and program deliverables
* Hands on experience of modifying existing software to correct errors and adapt to new hardware
* In depth knowledge of analyzing user needs and software requirements to determine feasibility of design
* Demonstrated ability to identify and resolve code-level problems
* Fast learner and independent with strong leadership and critical thinking skills
* Exceptional abilities in VLSI design and troubleshooting
* High grasp of computer languages, including C and C++

**RELEVANT COURSEWORK**

* Quartus II
* Mentor Graphics EDA tools
* MPLAB IDE
* Microsoft Project
* PSPICE
* Mathlab
* MS-DOS DEBUG program
* Programming in C/C++
* Multisim
* Nios II

**SKILLS, ABILITIES & COMPETENCIES**

* Creative Thinking Ability – can generate new idea
* Strong **programming** and **coding skills**
* Good exposure to **web development**
* Strong knowledge of creating **multi threaded applications** in **electronic environment**
* Excellent ability to take directions
* Exceptional ability to **troubleshoot common** and **complex software** issues
* High Responsible - can work cooperatively with others and contribute to a group with **ideas**, **suggestions**, and **effort**
* Can participate in all aspects of semiconductor **VLSI design**, including specification, architectural development, **transistor level design**, layout supervision, chip level verification, and lab validation applying knowledge in communication theory and systems, circuits, noise analysis and discrete time and continuous time signal processing skills
* Experience with cultural diversity – can work well with men and women of a verity of social and educational background
* Strong verbal communication skills and excellent writing ability
* **Computer Skills**: MS Word, Excel, Access, Publisher and Internet
* **Speak** and **write** fluency in English and Malay
* **Leadership quality**
* **Smart worker**
* **Interested in updating knowledge through continuous learning**

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**WORKING EXPERIENCE**

**RF Engineer** *(2015)*

**ZTE (MALAYSIA) CORPORATION SDN. BHD. –** Subang Hi-Tech Park

Responsibilities:

* Support Radio network planning & optimizing projects of GSM / UMTS+HSPA / NETMAX / LINUX / NDS SERVER / SSH / LTE
* Provide Radio Coverage & Capacity Dimensioning, RNC& BSC planning and Optimization (KPI), performance monitoring.
* Communicate with the high level customer, Project Manager, RF manager.
* Investigate and troubleshoot network quality problems.
* Radio Parameters fine tuning.
* Assist PM with RF team organization setup, resource planning and tasks allocation; to setup RF processes and supervise project RF part, ensure the network performance compiles with contract.